

# A Generalized Digraph Model for Expressing Dependencies

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Jean-François Monin, Sophie Quinton

The Inria logo is written in a stylized, cursive font with a color gradient from red to orange.

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# Overview of this talk

Context and motivation

A generalized digraph (GD) model

A RTA for the GD model

Conclusion and future work

# Context and motivation

## Context

Computer assisted verification of response time analyses (RTAs)

- ▶ Prosa library based on the Coq proof assistant

- ▶ CASERM and RT-PROOFS projects  ■

- Formal specifications
- Machine-checked proofs of RTA theories
- **But time consuming!**

# Context and motivation

## ► Problem

How to reduce proof efforts?

A RTA depends on:

- Platform: uniprocessor, multiprocessor *etc.*
- Scheduling policy: FPP, FPNP, EDF *etc.*
- Task model: periodic, sporadic, digraph *etc.*

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## ▶ Objective

1. Formally certify\* one RTA for a general model;
2. Instantiate it to specific task models

\* verify in the Coq proof assistant

# This paper

A RTA for

- ▶ Platform: **uniprocessor**
- ▶ Scheduling policy: **FP (Preemptive/Non-preemptive)**
- ▶ Task model: **offsets, arrival curves, digraph *etc.***

Generalized digraph model

# This paper

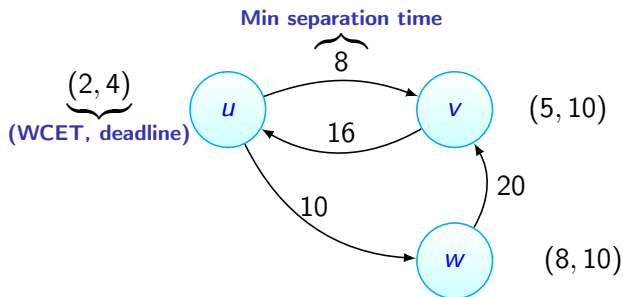
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Generalized digraph model

**3 small additional extensions** on top of the **digraph model**

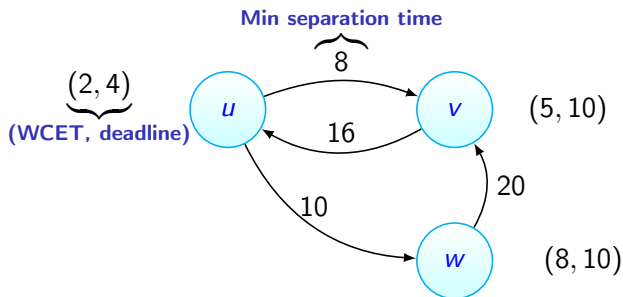
# The standard digraph model



Syntax:



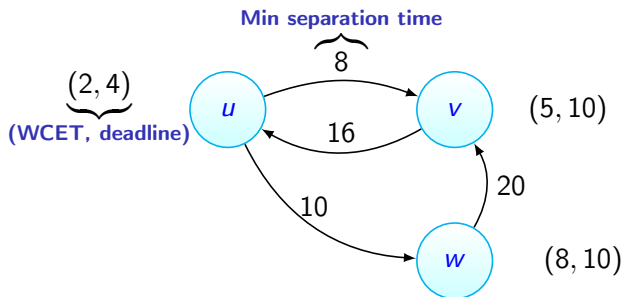
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## Syntax:

- ▶ Vertex (type of jobs):  $(\mathbb{N}^+, \mathbb{N}^+)$

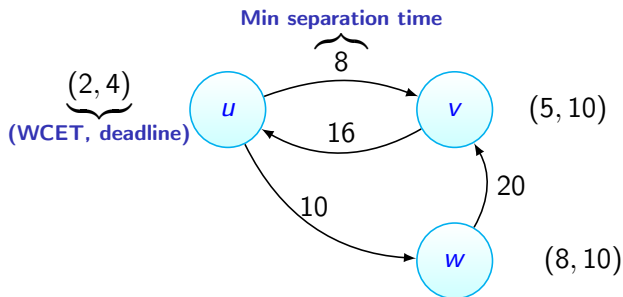
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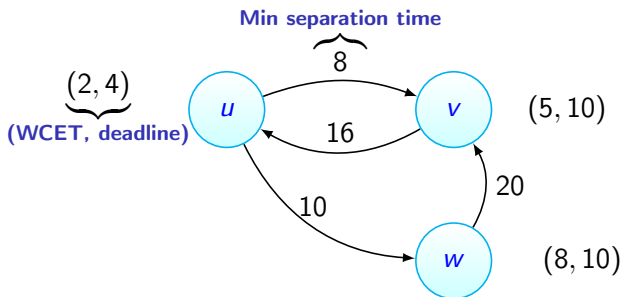
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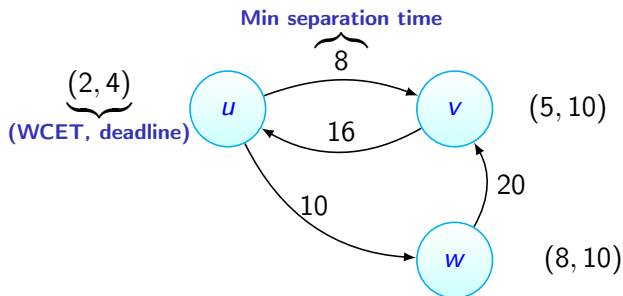


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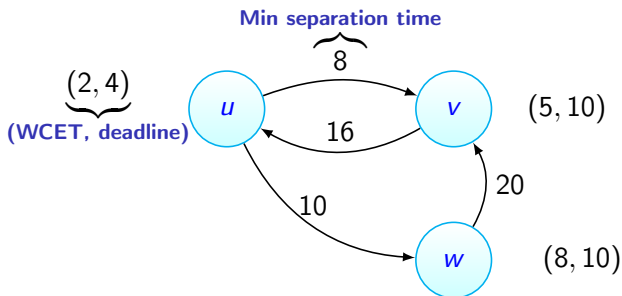
Def **Path**: a list of ordered vertices e.g.,  $[v, u, w]$ ,  $[v, w]$

# The standard digraph model



Semantics: a task  $\rightarrow$  paths  $\rightarrow$  arrival sequences

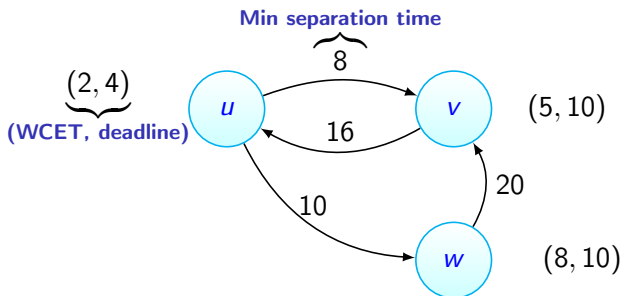
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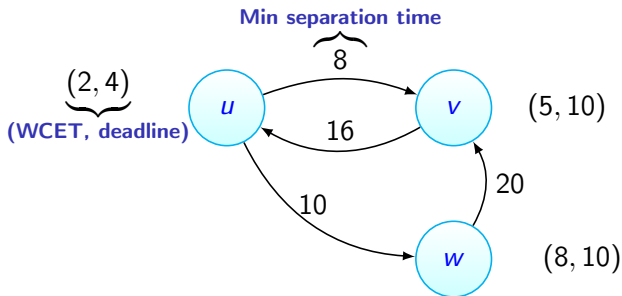
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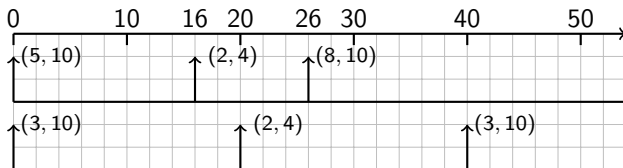
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Example: arrival sequences of path  $[v, u, w]$





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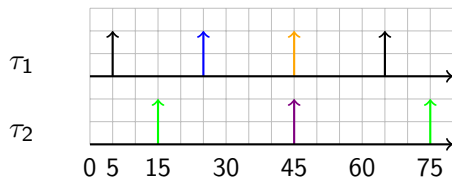
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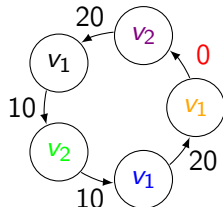
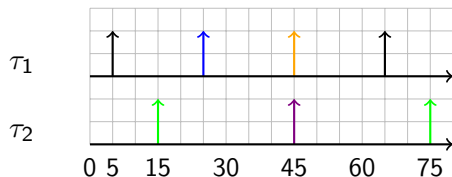
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Job Level Fixed Priority Limited Preemption

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## Job Level Fixed Priority Limited Preemption

**Example:** Consider a vertex  $v$ , its WCET = 8 time units

- FPP: [1,1,1,1,1,1,1,1] (8 segments)
- FPNP: [8] (1 segment)
- FPLP: [4,4] (2 segments)

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$$wcrt(v) := \max_{\pi \in \Pi_{\Sigma}} \left\{ \max_{j: v, j \in \rho, \rho \sim \pi} \{rt_j\} \right\}$$

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**Two challenges:**

- ▶ In first max:  $\Pi_{\Sigma}$  possibly infinite
- ▶ In second max:  $j : v, j \in \rho, \rho \sim \pi$  possibly infinite

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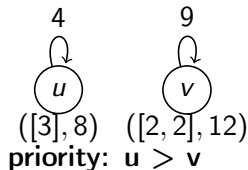
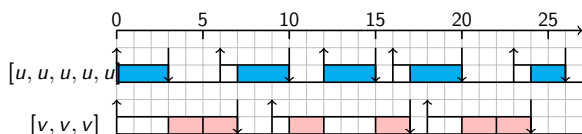
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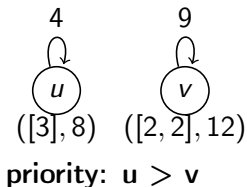
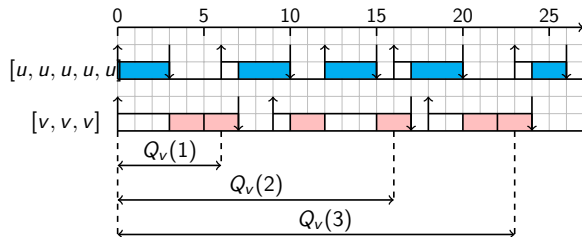


$\Rightarrow$  vertices  $\in hep(p)$  released in  $[t_1, t_2[$  completed by  $t_2$

# A RTA for the GD model

Another concept for the RTA

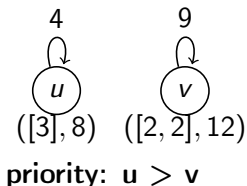
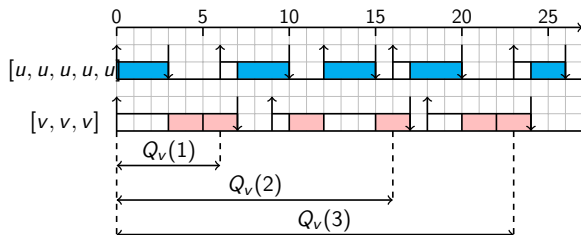
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In busy window  $[0, 26]$ :

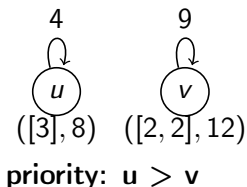
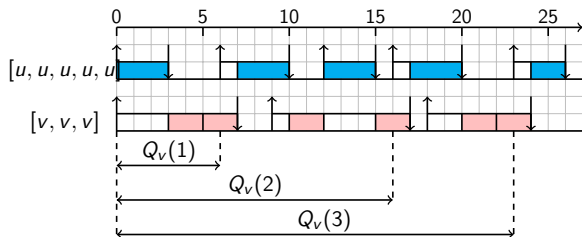
$$RT_v(2) = Q_v(2) - \theta_v(2) + \text{last}(\vec{C}(v)) - 1$$

where  $\theta_v(2)$  is the duration between 0 and the 2nd activation of  $v$

# A RTA for the GD model

Another concept for the RTA

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In busy window  $[0, 26]$ :

$$\max_{q \leq 3} \left\{ Q_v(q) - \theta_v(q) + \text{last}(\vec{C}(v)) - 1 \right\}$$

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**Step 3:** Finally, compute an upper bound on  $wcrt(v)$ .

$$\max_{\pi \in \Pi_\Sigma} \{RT_\pi(v)\}$$

# Discussion

- ▶ **Our RTA covers many RTAs**
  - Uniprocessor
  - FPP/FPNP scheduling policies
  - Task model (arbitrary deadline):
    - ▶ Existing task models: offsets, arrival curves, digraph,...
    - ▶ New task models can be captured by GD:  
e.g., Multiframe with offsets, arrival curves with different WCET...
- ▶ **A general proof structure of RTAs**  
Abstract path as a abstract workload function

# Conclusion and future work

## Summary

- ▶ A generalized digraph model
- ▶ A RTA for that model
- ▶ A correctness proof of that RTA amenable to its formalization in Coq

# Conclusion and future work

## Future work

- ▶ The complete Coq formalization of the presented RTA
  - instantiate more specific RTAs
  - A formal comparison of our proposed analysis with the existing RTAs (e.g., RTAs of digraph model)
  - A study of **approximated methods**
  
- ▶ Extensions to more complex models, e.g., task chains

Thank you for your attention!  
Questions/Comments